

Appln No. 10/026,109

Amdt date June 14, 2005

Reply to Office action of March 14, 2005

**Amendments to the Specification:**

Please replace the paragraph beginning on page 8, line 5 with the following:

According to various embodiments, the values i and j are initialized to a value of zero at 301. At 303, i is incremented by 1 and any carry out is ignored (mod 256 function). At 305, j gets the value in the S register at address i added to the previous value of j. Again, any carry out is ignored. At 307, the values in the S register at address i and address j are swapped. At 309, t gets the value of the register at address i added to the value of the register at address j, ignoring again any carry out. The generated key stream byte is the value of the register at address t.